

**PATENT NUMBER**

Class	Subclass
	1

ISSUE CLASSIFICATION

<p>  </p> <p> <b>SCANNED</b> <u>                    </u> <b>Q.A.</b> <u>                    </u> </p>	<p> <b>PATENT DATE</b> </p>
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APPLICATION NO. 09/759557	CONT/PRIOR D	CLASS 324 714	SUBCLASS 734	ART UNIT 2858 2133	EXAMINER Deady
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TITLE	APPLICANTS
1. <b>Chairman</b>	1. <b>Mr. J. H. ...</b>
2. <b>Secretary</b>	2. <b>Mr. J. H. ...</b>
3. <b>Treasurer</b>	3. <b>Mr. J. H. ...</b>
4. <b>Member</b>	4. <b>Mr. J. H. ...</b>
5. <b>Member</b>	5. <b>Mr. J. H. ...</b>
6. <b>Member</b>	6. <b>Mr. J. H. ...</b>
7. <b>Member</b>	7. <b>Mr. J. H. ...</b>
8. <b>Member</b>	8. <b>Mr. J. H. ...</b>
9. <b>Member</b>	9. <b>Mr. J. H. ...</b>
10. <b>Member</b>	10. <b>Mr. J. H. ...</b>
11. <b>Member</b>	11. <b>Mr. J. H. ...</b>
12. <b>Member</b>	12. <b>Mr. J. H. ...</b>
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94. <b>Member</b>	94. <b>Mr. J. H. ...</b>
95. <b>Member</b>	95. <b>Mr. J. H</b>

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Marc Erett  
James Mears  
Mark Sauerwald

Algorithmic test pattern generator, with built-in-self-test (BIST) capabilities, for functional testing of a circuit

PTO-2040  
12/89

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

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	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____     	_____ (Assistant Examiner) (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
	_____ (Primary Examiner) (Date)			<b>ISSUE FEE</b>	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)			<b>ISSUE BATCH NUMBER</b>	
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